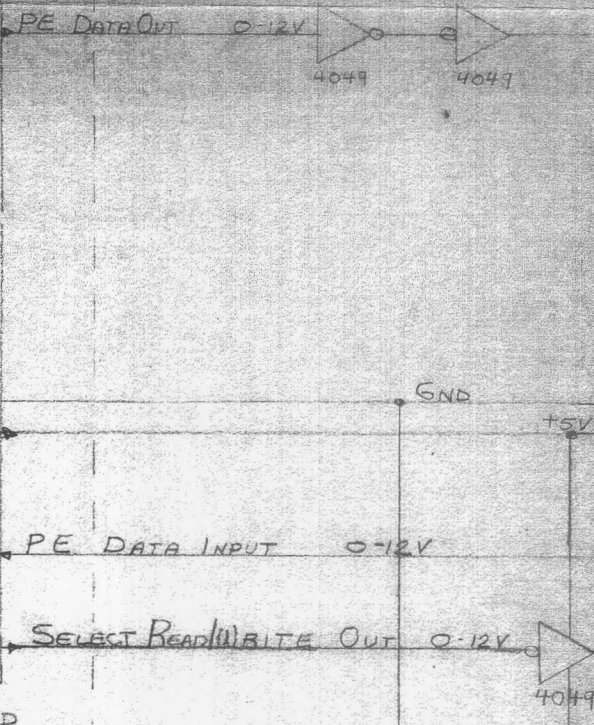


ALL SIGNALS ABOVE
ARE 0-5VDC
SEE CS-400A MANUAL
FOR SEQUENCE

MODIFIED
E/D BOARD
(300-0256)

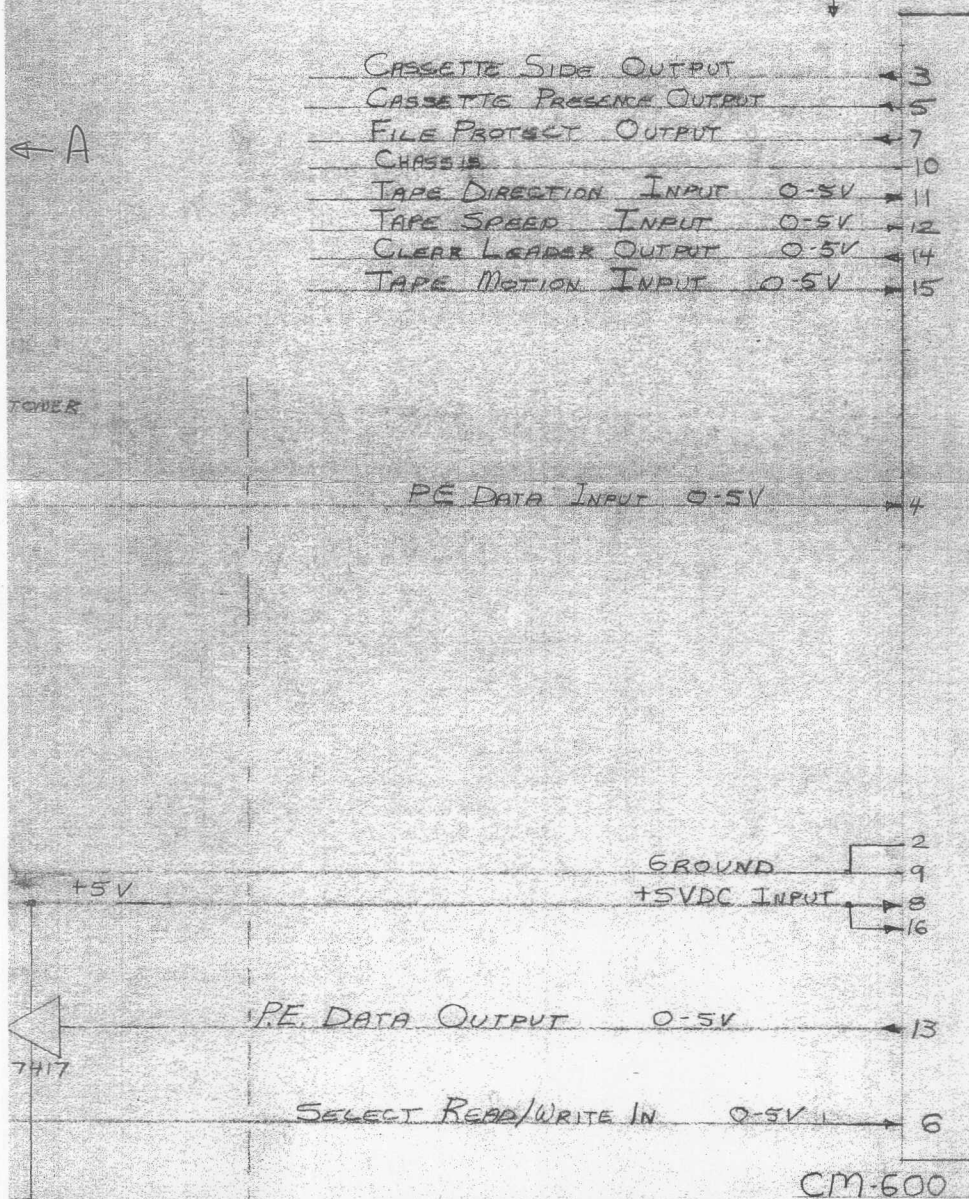
NOTE: DATA RATE NOW 2.4 KBAUD (NOT 8 KBAUD) AND
CELL WIDTH IS 416 μ S (NOT 125 μ S). ALSO START DELAY
IS 140 ms (NOT 70 ms AS STATED IN CS-400A MANUAL)

INTERFACE LOGIC SUPPLIED BY



DATE	SYM	ECO NO.	APPR.

SEE CM 600 MANUAL
FOR DETAILS OF SIGNALS BELOW



REQUIRED SPECIFICATIONS	UNIT OF MEASURE	TOLERANCES (EXCEPT AS NOTED)	BRAEMAR COMPUTER DEVICES, INC.		
	0.000 MM 0.000 INCHES	DECIMAL ±X = .50 MM/.02 IN. ±XX = .25 MM/.01 IN. ±XXX = .13 MM/.005 IN.	SCALE	DRAWN BY JCR	4-22-78
MAT'L.	FRACTIONAL	±1/32	TITLE: INTERFACE BETWEEN CM600 AND MODIFIED E/D BOARD		
	ANGULAR		DWG SIZE C	DRAWING NUMBER SK 10080	PAGE OF